

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chi-Hui LIN)
) Group: Not yet assigned
)
Serial No.: Not yet assigned)
) Examiner: Not yet assigned
Filed: Concurrently herewith)
) Our Ref: B-5158 621093-8
)
For: "STACKED GATE FLASH MEMORY)
DEVICE AND METHOD OF)
FABRICATING THE SAME") Date: July 16, 2003

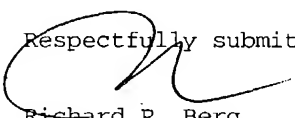
Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CROSS-REFERENCE TO RELATED APPLICATIONS

Sir:

This application is related to the U.S. patent application
invented by Chi-Hui LIN entitled "Split Gate Flash Memory Device
and Method of Fabricating the Same," which application is being
filed on July 16, 2003.

Respectfully submitted,


Richard P. Berg
Attorney for Applicant
Reg. No. 28,145

LADAS & PARRY
5670 Wilshire Boulevard
Suite 2100
Los Angeles, CA 90036
Telephone: (323) 934-2300
Telefax: (323) 934-0202